

# Section 7

## Schematic Diagrams

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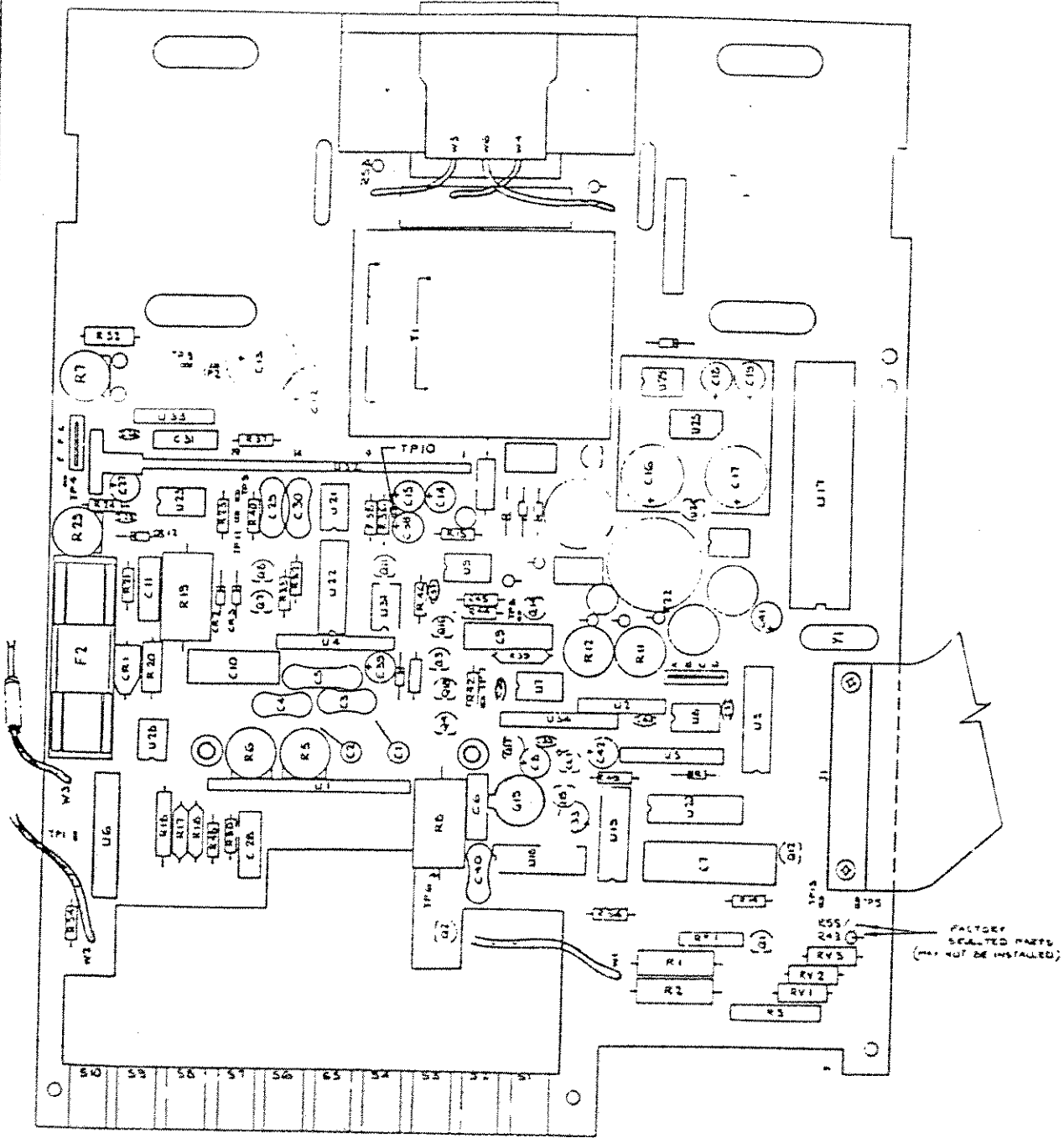
1


AC X 1	AC buffer gain of 1 command
AC X 100	AC Buffer gain of 100 command
AZ	Auto Zero
BP	Back plane drive signal, 50 Hz square wave
BT	Low Battery, indicates that the rechargeable batteries in the -01 Option need recharging
dB	Decibels
DE(+R)	De-integrate plus reference used with a negative input
DE(-R)	De-integrate minus reference used with a positive input
DP	Decimal Point
EXT INT	External Interrupt, stops the counter in the microcomputer
F <sub>a</sub>	Function inputs to microcomputer
F <sub>b</sub>	
F <sub>c</sub>	
F <sub>d</sub>	
HOLD	A 52 $\mu$ sec (maximum) period immediately following INT
HV	High Voltage, comes on when the instrument measures 40V or greater
INT	Integrate
LCD	Liquid Crystal Display
LO	Defines front panel selection of a function/range requiring an A/D gain of 10
OL	A 5 msec period at the beginning of auto zero when an overrange is detected
P.O.R.	Power On Reset, a 500 msec pulse at turn on which initializes the A/D
REL	Relative, indicates that the readings displayed are relative to the input applied when the RELATIVE switch was set to ON
RNG a	Range inputs to microcomputer
RNG b	
RNG c	
S	Siemens, 1/ $\Omega$
ST0	Five sequential Strobe pulses
ST1	
ST2	
ST3	
ST4	
T&H	Touch & Hold
W	BCD data lines
X	
Y	
Z	
Z	Impedance

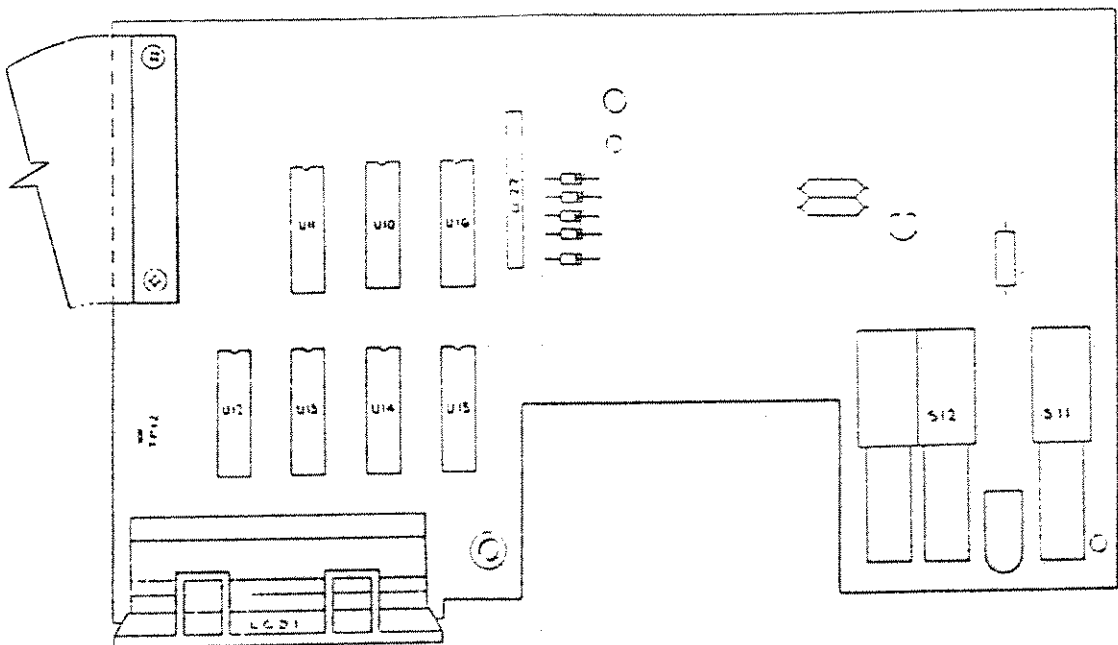
Figure 7-3. Mnemonics



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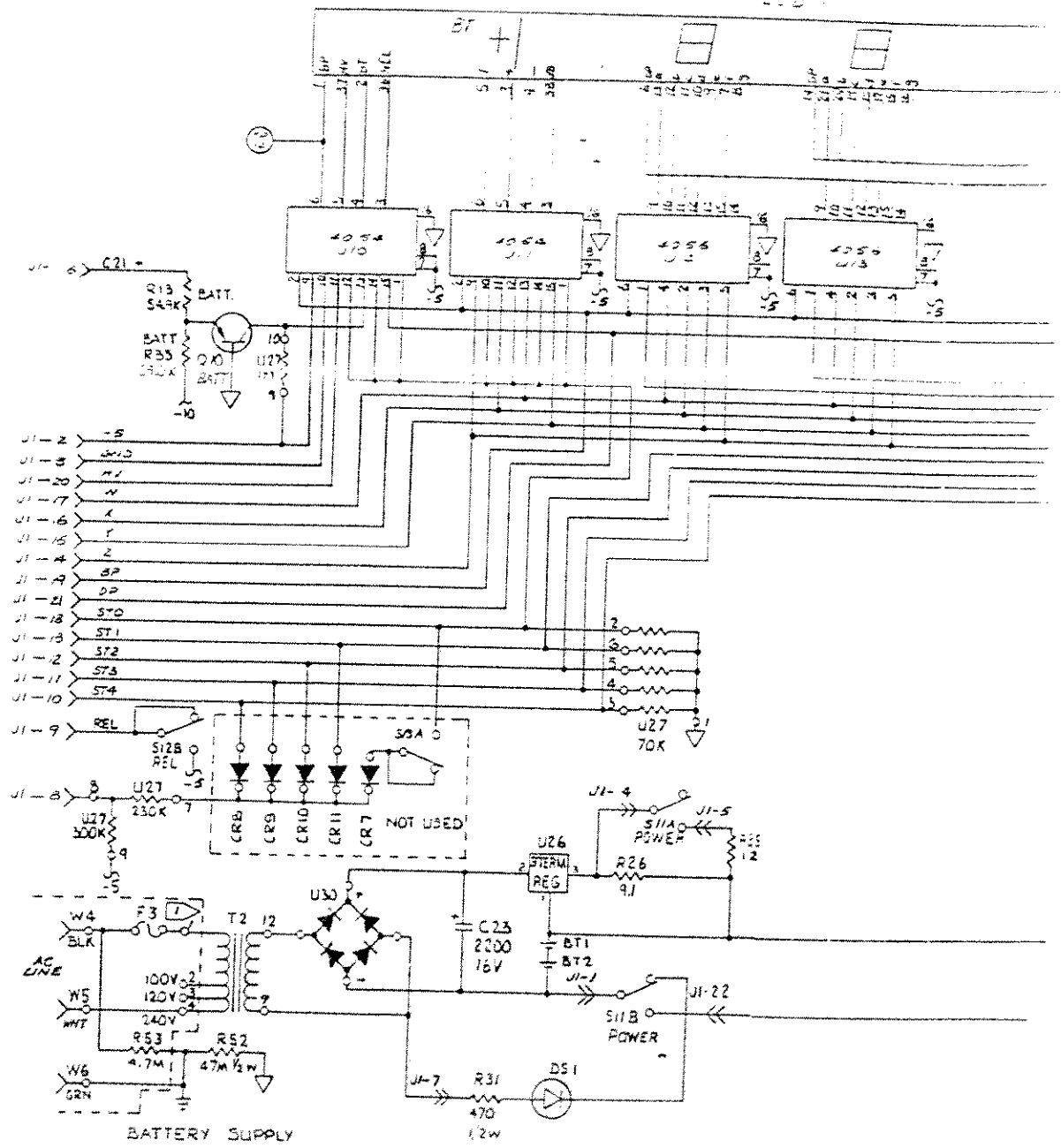
 **CAUTION**  
SUBJECT TO DAMAGE BY  
STATIC ELECTRICITY



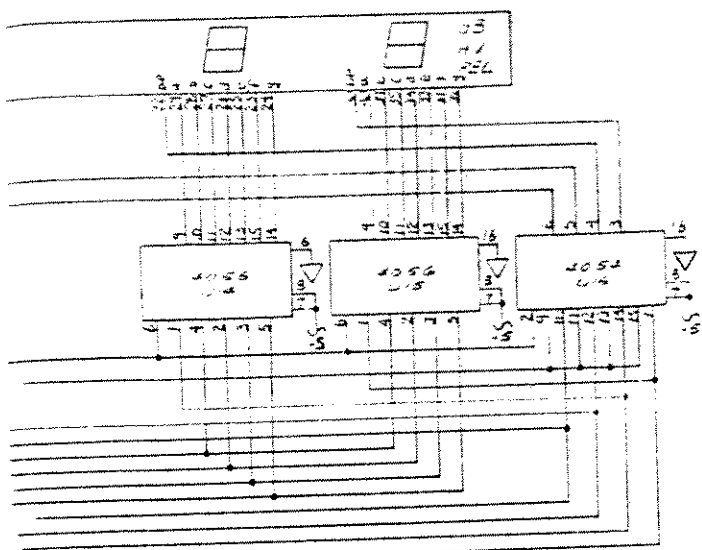
FOR SCHEMATIC DIAGRAM, SEE FIGURE 7-1.

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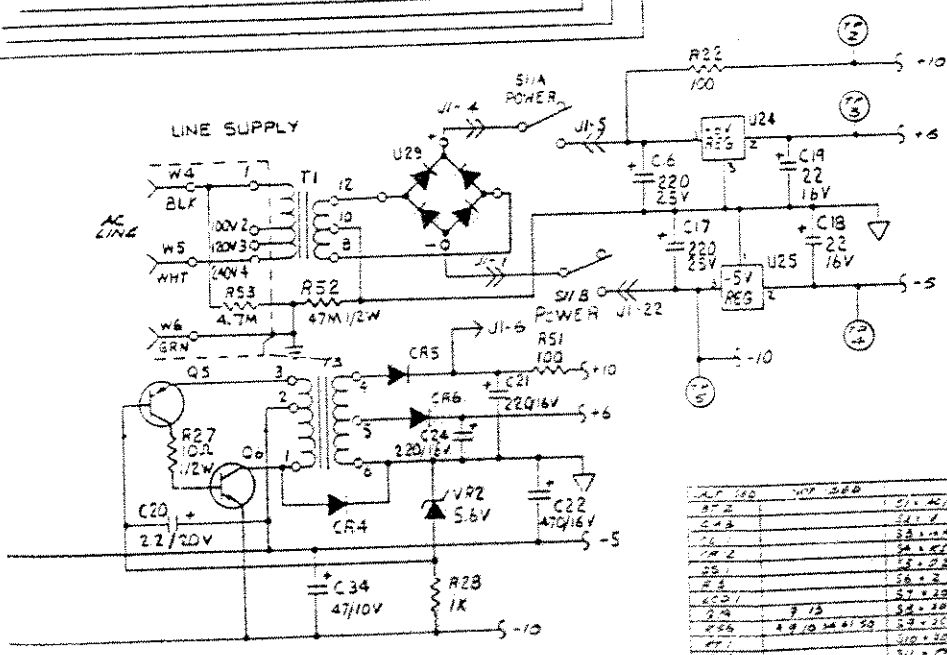
Figure 7-1. A1 Main PCB Assembly, Line Supply



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- NOTES
- 1. 1/6A FOR 100V AND 120V CONFIGURATION.  
1.32A FOR 120V CONFIGURATION.
  - 2. ALL RESISTANCE VALUES ARE IN OHMS.  
ALL CAPACITANCE VALUES ARE IN MICROFARADS.

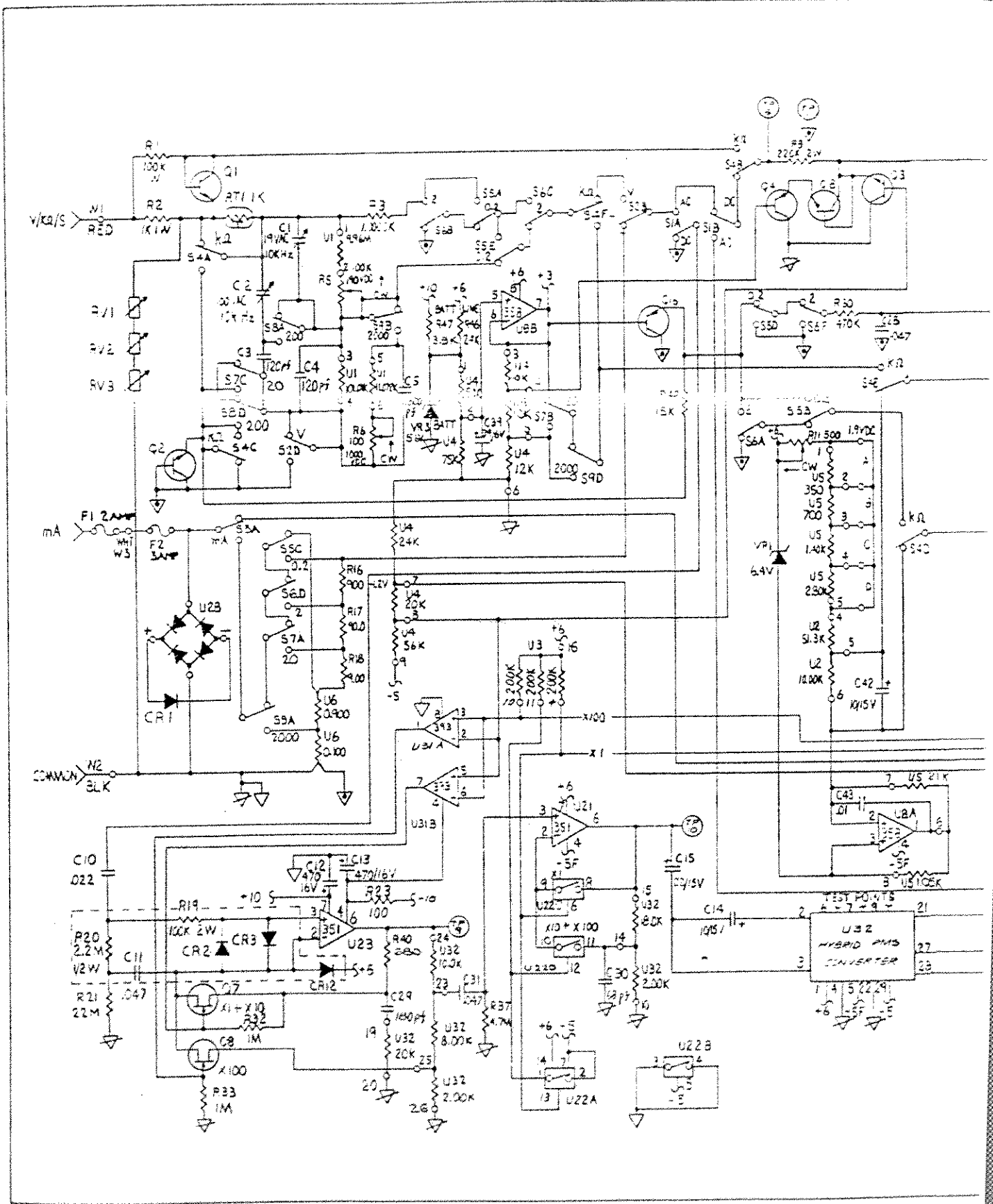


REF ID	VAL	UNIT
1	100	Ω
2	100	Ω
3	100	Ω
4	100	Ω
5	100	Ω
6	100	Ω
7	100	Ω
8	100	Ω
9	100	Ω
10	100	Ω
11	100	Ω
12	100	Ω
13	100	Ω
14	100	Ω
15	100	Ω
16	100	Ω
17	100	Ω
18	100	Ω
19	100	Ω
20	100	Ω
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77	100	Ω
78	100	Ω
79	100	Ω
80	100	Ω
81	100	Ω
82	100	Ω
83	100	Ω
84	100	Ω
85	100	Ω
86	100	Ω
87	100	Ω
88	100	Ω
89	100	Ω
90	100	Ω
91	100	Ω
92	100	Ω
93	100	Ω
94	100	Ω
95	100	Ω
96	100	Ω
97	100	Ω
98	100	Ω
99	100	Ω
100	100	Ω

FOR LINE VERSION COMPONENT LOCATIONS, SEE FIGURE 5-2 OR 7-1.  
 FOR .01 BATTERY OPTION COMPONENT LOCATIONS, SEE FIGURE 601-2 OR 7-3.  
 SWITCHES ARE SHOWN IN THEIR RELAXED (OUT) POSITION.

Figure 7-1. A1 Main PCB Assembly (cont)





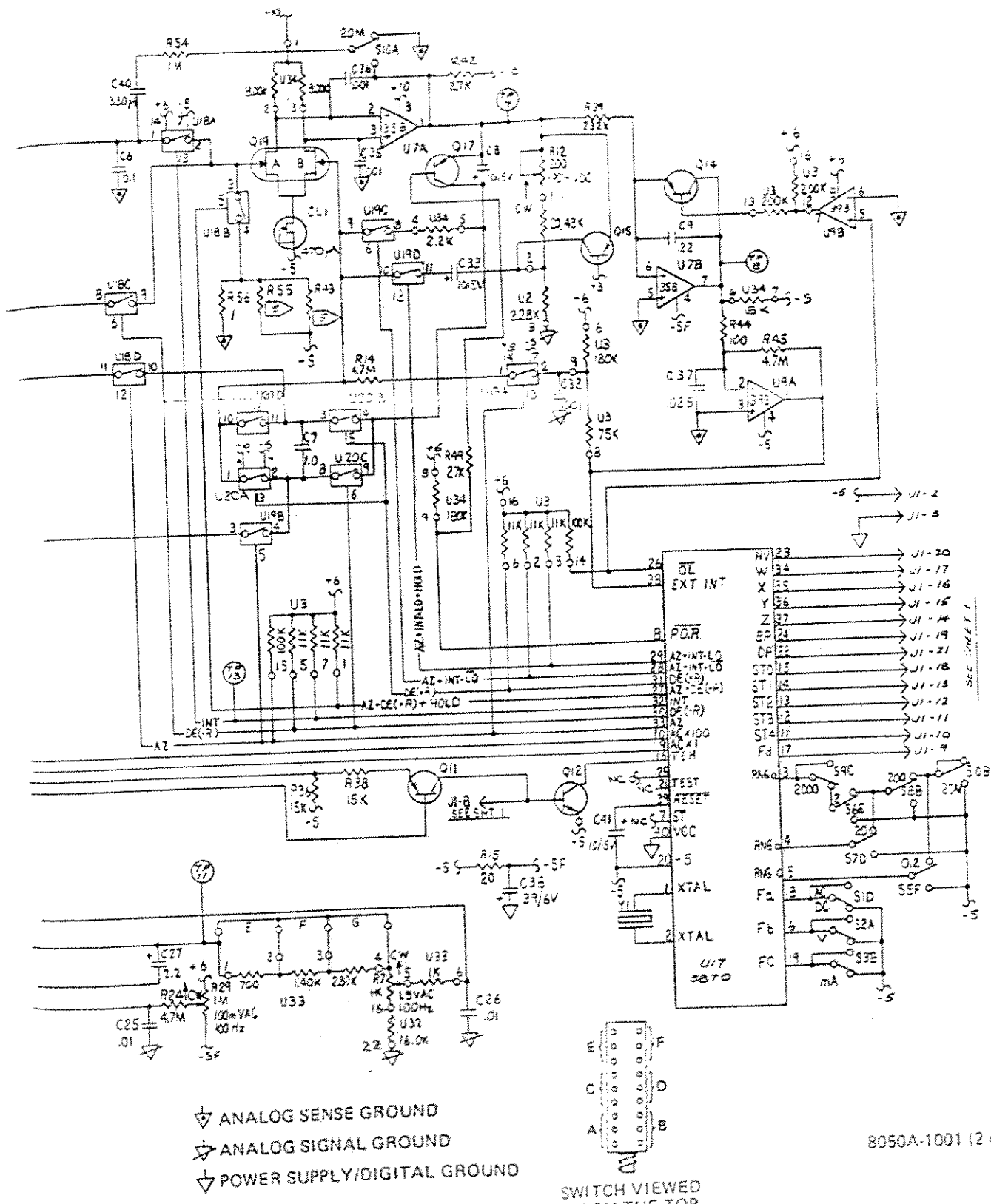
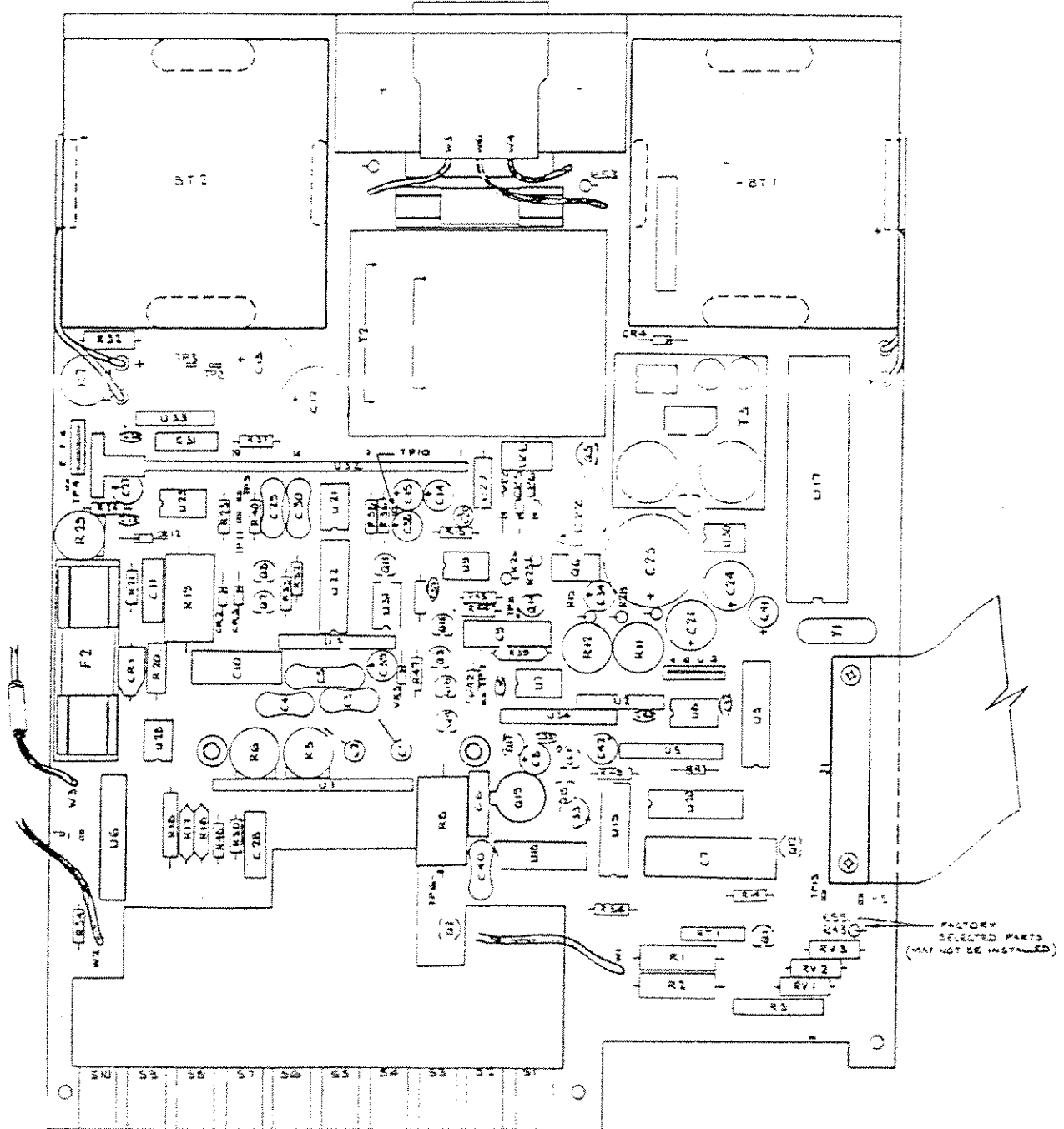
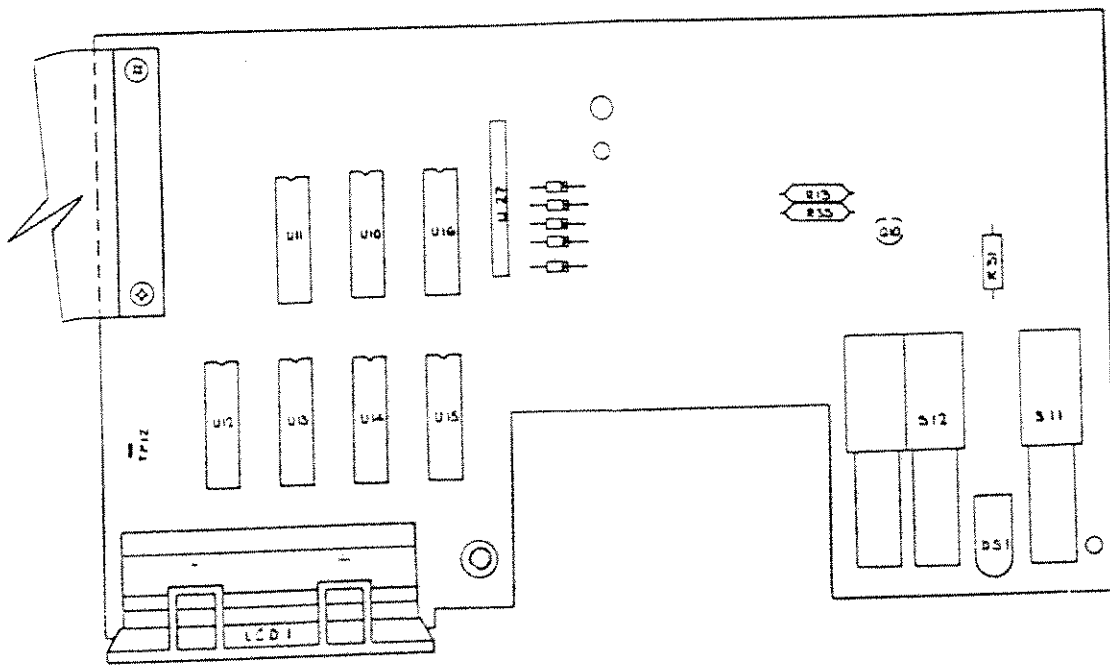


Figure 7-1. A1 Main PCB Assembly (cont)



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FOR SCHEMATIC DIAGRAM, SEE FIGURE 7-1.

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Figure 7-2. A1 Main PCB Assembly, Battery Option